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**RESPONSE UNDER 37 C.F.R. 1.116  
EXPEDITED PROCEDURE  
EXAMINING GROUP 2818**

ATTORNEY DOCKET NO. 5649-952

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re: Jae-goo Lee	Group Art Unit: 2818
Serial No.: 10/068,148	Examiner: Son Luu Mai
Filed: February 6, 2002	Confirmation No.: 7507
For: INTEGRATED CIRCUIT MEMORY DEVICES WITH PER-BIT REDUNDANCE AND METHODS OF OPERATING SAME	

Date: August 21, 2003

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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**AMENDMENT AFTER FINAL ACTION  
PURSUANT TO 37 C.F.R. § 1.116**

RECEIVED  
AUG 27 2003  
TECHNICAL CLERK 2800

Sir:

This Amendment is responsive to the Final Office Action of June 23, 2003. Please amend the above-identified application as follows.

If any extension of time for the accompanying response or submission is required, Applicant requests that this be considered a petition therefor. The Commissioner is hereby authorized to charge any additional fee, which may be required, or credit any refund, to our Deposit Account No. 50-0220.